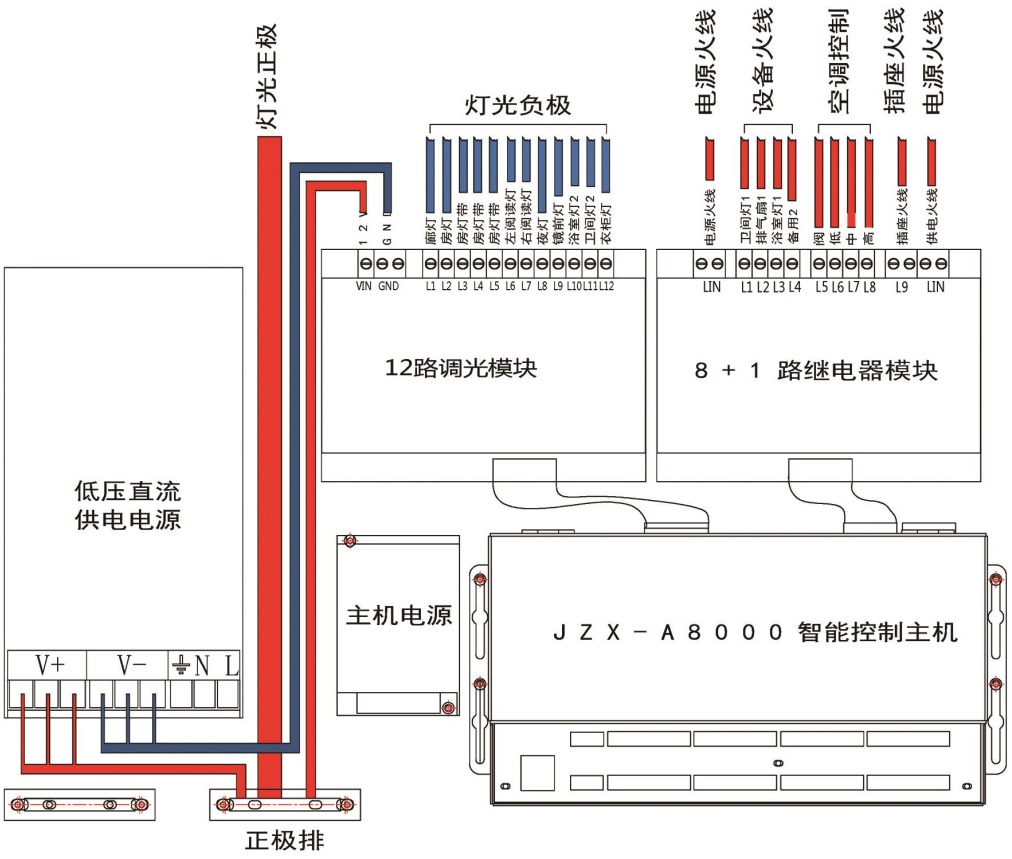
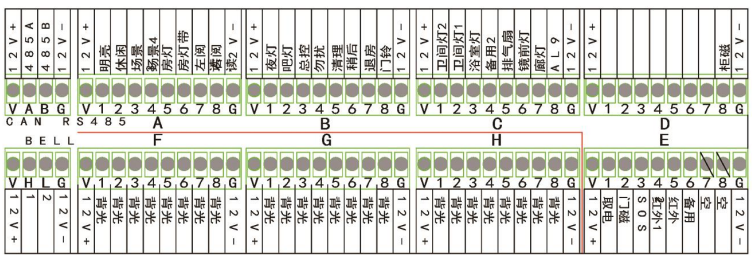
ADV型控制器线路示意图





输入输出端子定义